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PATENT APPLICATION

**RESPONSE UNDER 37 CFR §1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER ART UNIT 2826**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Terunao HANAOKA et al.

Application No.: 09/700,464

Filed: November 15, 2000

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE THEREOF,
CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

Group Art Unit: 2826

Examiner: L. Andujar

Docket No.: 107284

*10/10 (MAY)
MAY 30
6/5/03*

AMENDMENT AFTER FINAL REJECTION UNDER 37 CFR §1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the February 27, 2003 Office Action, please consider the following:

Amendments to the Claims are reflected in the listing of claims;

Remarks.

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